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Search Results - Record(s) 1 through 20 of 31 returned.

☐ 1. Document ID: US 20030005208 A1**Using default format because multiple data bases are involved.**

L31: Entry 1 of 31

File: PGPB

Jan 2, 2003

PGPUB-DOCUMENT-NUMBER: 20030005208

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030005208 A1

TITLE: Synchronous integrated circuit device

PUBLICATION-DATE: January 2, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Farmwald, Michael	Berkeley	CA	US	
Horowitz, Mark	Palo Alto	CA	US	

US-CL-CURRENT: 710/307

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw De
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☐ 2. Document ID: US 20020161988 A1

L31: Entry 2 of 31

File: PGPB

Oct 31, 2002

PGPUB-DOCUMENT-NUMBER: 20020161988

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020161988 A1

TITLE: Data processing circuits and interfaces

PUBLICATION-DATE: October 31, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Barlow, Stephen John	Cambridge		GB	
Morfey, Alistair Guy	Cambridge		GB	
Collier, James Digby	Cambridge		GB	

US-CL-CURRENT: 712/221; 712/245

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Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw D
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☐ 3. Document ID: US 20010016861 A1

L31: Entry 3 of 31

File: PGPB

Aug 23, 2001

PGPUB-DOCUMENT-NUMBER: 20010016861

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20010016861 A1

TITLE: Apparatus for performing packed shift operations

PUBLICATION-DATE: August 23, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lin, Derrick Chu	Foster City	CA	US	
Minocha, Punit	Santa Clara	CA	US	
Peleg, Alexander D.	Haifa	CA	IL	
Yaari, Yaakov	Haifa	CA	IL	
Mittal, Millind	South San Francisco		US	
Mennemeier, Larry M.	Boulder Creek		US	
Eitan, Benny	Haifa		IL	

US-CL-CURRENT: 708/209; 708/518, 712/22

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw D
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☐ 4. Document ID: US 6738793 B2

L31: Entry 4 of 31

File: USPT

May 18, 2004

US-PAT-NO: 6738793

DOCUMENT-IDENTIFIER: US 6738793 B2

TITLE: Processor capable of executing packed shift operations

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw D
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☐ 5. Document ID: US 6701336 B1

L31: Entry 5 of 31

File: USPT

Mar 2, 2004

US-PAT-NO: 6701336

DOCUMENT-IDENTIFIER: US 6701336 B1

TITLE: Shared galois field multiplier

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw D
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☐ 6. Document ID: US 6606532 B1

L31: Entry 6 of 31

File: USPT

Aug 12, 2003

US-PAT-NO: 6606532

DOCUMENT-IDENTIFIER: US 6606532 B1

TITLE: Method of manufacturing system LSI and system LSI manufactured using the method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Searches	Attachments	Claims	KWIC	Draw De
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☐ 7. Document ID: US 6567834 B1

L31: Entry 7 of 31

File: USPT

May 20, 2003

US-PAT-NO: 6567834

DOCUMENT-IDENTIFIER: US 6567834 B1

TITLE: Implementation of multipliers in programmable arrays

Full	Title	Citation	Front	Review	Classification	Date	Reference	Searches	Attachments	Claims	KWIC	Draw De
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☐ 8. Document ID: US 6427179 B1

L31: Entry 8 of 31

File: USPT

Jul 30, 2002

US-PAT-NO: 6427179

DOCUMENT-IDENTIFIER: US 6427179 B1

TITLE: System and method for protocol conversion in a communications system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Searches	Attachments	Claims	KWIC	Draw De
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☐ 9. Document ID: US 6311263 B1

L31: Entry 9 of 31

File: USPT

Oct 30, 2001

US-PAT-NO: 6311263

DOCUMENT-IDENTIFIER: US 6311263 B1

**** See image for Certificate of Correction ****

TITLE: Data processing circuits and interfaces

Full	Title	Citation	Front	Review	Classification	Date	Reference	Searches	Attachments	Claims	KWIC	Draw De
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☐ 10. Document ID: US 6282554 B1

L31: Entry 10 of 31

File: USPT

Aug 28, 2001

US-PAT-NO: 6282554

DOCUMENT-IDENTIFIER: US 6282554 B1

TITLE: Method and apparatus for floating point operations and format conversion operations

Full	Title	Citation	Front	Review	Classification	Date	Reference	Attachments	Attachments	Claims	KVMC	Drawn De
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☐ 11. Document ID: US 6237015 B1

L31: Entry 11 of 31

File: USPT

May 22, 2001

US-PAT-NO: 6237015

DOCUMENT-IDENTIFIER: US 6237015 B1

TITLE: Method for the production of a parameter J0 associated with the implementation of modular operations according to the Montgomery method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Attachments	Attachments	Claims	KVMC	Drawn De
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☐ 12. Document ID: US 6128696 A

L31: Entry 12 of 31

File: USPT

Oct 3, 2000

US-PAT-NO: 6128696

DOCUMENT-IDENTIFIER: US 6128696 A

TITLE: Synchronous memory device utilizing request protocol and method of operation of same

Full	Title	Citation	Front	Review	Classification	Date	Reference	Attachments	Attachments	Claims	KVMC	Drawn De
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☐ 13. Document ID: US 6122651 A

L31: Entry 13 of 31

File: USPT

Sep 19, 2000

US-PAT-NO: 6122651

DOCUMENT-IDENTIFIER: US 6122651 A

TITLE: Method and apparatus for performing overshifted rotate through carry instructions by shifting in opposite directions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Attachments	Attachments	Claims	KVMC	Drawn De
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☐ 14. Document ID: US 6023742 A

L31: Entry 14 of 31

File: USPT

Feb 8, 2000

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US-PAT-NO: 6023742

DOCUMENT-IDENTIFIER: US 6023742 A

**** See image for Certificate of Correction ****

TITLE: Reconfigurable computing architecture for providing pipelined data paths

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstracts	Attachments	Claims	KWIC	Draw De
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☐ 15. Document ID: US 6018756 A

L31: Entry 15 of 31

File: USPT

Jan 25, 2000

US-PAT-NO: 6018756

DOCUMENT-IDENTIFIER: US 6018756 A

TITLE: Reduced-latency floating-point pipeline using normalization shifts of both operands

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstracts	Attachments	Claims	KWIC	Draw De
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☐ 16. Document ID: US 6012077 A

L31: Entry 16 of 31

File: USPT

Jan 4, 2000

US-PAT-NO: 6012077

DOCUMENT-IDENTIFIER: US 6012077 A

TITLE: Method and apparatus for indicating overflow status of bit-variable data employing pipelining adder

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstracts	Attachments	Claims	KWIC	Draw De
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☐ 17. Document ID: US 5506992 A

L31: Entry 17 of 31

File: USPT

Apr 9, 1996

US-PAT-NO: 5506992

DOCUMENT-IDENTIFIER: US 5506992 A

TITLE: Distributed processing system with asynchronous communication between processing modules

Full	Title	Citation	Front	Review	Classification	Date	Reference	Abstracts	Attachments	Claims	KWIC	Draw De
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☐ 18. Document ID: US 5471628 A

L31: Entry 18 of 31

File: USPT

Nov 28, 1995

US-PAT-NO: 5471628

DOCUMENT-IDENTIFIER: US 5471628 A

TITLE: Multi-function permutation switch for rotating and manipulating an order of bits of an input data byte in either cyclic or non-cyclic mode

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequence	Abstracts	Claims	KWMC	Draw De
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☒ 19. Document ID: US 5293558 A

L31: Entry 19 of 31

File: USPT

Mar 8, 1994

US-PAT-NO: 5293558

DOCUMENT-IDENTIFIER: US 5293558 A

TITLE: Multiplication, division and square root extraction apparatus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequence	Abstracts	Claims	KWMC	Draw De
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☐ 20. Document ID: US 4999796 A

L31: Entry 20 of 31

File: USPT

Mar 12, 1991

US-PAT-NO: 4999796

DOCUMENT-IDENTIFIER: US 4999796 A

TITLE: Sticky bit detection and shifting logic

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequence	Abstracts	Claims	KWMC	Draw De
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